



A Charutar Vidya Mandal's Institution
V.P. & R.P.T.P. SCIENCE COLLEGE
(AUTONOMOUS) (Grant-in-Aid)
Affiliated to Sardar Patel University
VALLABH VIDYANAGAR-388120



Bachelor of Science (Semester VI)

Internal Examination - 2026

US06MIELE03: 8 Bit Microprocessor Programming -2

Date and Day: 09/03/2026, Monday

Total marks :20

Time:10:45 am to 11:45 am

Q-1 Choose the correct answer (Attempt all)

(06)

- 1 Which of the following can be used as data storage?
(A) ROM. (C) RAM
(B) PROM (D) EPROM
- 2 'OUT' instruction belongs to which of the following group?
(A) Arithmetic instruction (C) Logical instruction
(B) Data transfer instruction (D) Machine control
- 3 If the contents of accumulator is A5 H, what it will be after execution of CMA?
(A) 5A H (C) FF H
(B) AA H (D) 00 H
- 4 Which of following is machine control instruction.
(A) RET (C) NOP
(B) JNC (D) ANA B
- 5 Which of the following is not an interrupt in 8085 micro controller?
(A) RST 4.5 (C) RST 6.5
(B) RST 5.5 (D) RST 7.5
- 6 The language that the computer can understand and execute is called ____?
(A) System program. (C) Machine Language.
(B) Assembly Language. (D) Application software.



Q-2 (A) List the Data transfer instructions and explain any two in detail.

(04)

(B) How many times the following loop will be executed?

(03)

MVI C, 10H

LOOP: DCRC

JNZ LOOP

HLT

OR

Q-2 (A) List the logical instructions and explain any two in detail.

(04)

(B) Differentiate between assembly language and machine language.

(03)

Q-3 List various Arithmetic instructions and explain each in details with examples.

(07)

OR

Q-3 Illustrate the steps necessary to add ten bytes of data stored in memory

(07)

locations from C100H and display the SUM on port 1. Draw necessary flow chart and write an assembly language program.

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Time:10:45 am to 10:55 am

Total marks :05

Q-1 Answer the following question with either true or false.

1 IN instruction is a type of logical instruction.

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2 JNC is two byte instruction.

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3 Carry flag is used internally.

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4 RET is machine control instruction.

[

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5 DAD B instruction is used for register pair addition.

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