



A Charutar Vidya Mandal's Institution
V.P. & R.P.T.P. SCIENCE COLLEGE (AUTONOMOUS)
(Grant-in-Aid)

Affiliated to Sardar Patel University
VALLABH VIDYANAGAR-388120

Bachelor of Science (Semester V)

Internal Examination - 2025

US05MIELE05: 8085 Microprocessor and Programming -1



Date and Day: 25/09/2025, Thursday

Time: 10:45 AM to 11:45 AM

Total marks: 20

Q-1 Choose the correct answer (Attempt all)

(06)

- 1 Which of following is a 16 bit register in 8085 μ p?
(A) Flag register. (C) Accumulator.
(B) Stack pointer. (D) Temporary register.
- 2 What is the full form of MPU?
(A) My Processing Unit (C) Micro Processing Unit
(B) Multiple Processing Unit (D) Macro Processing Unit
- 3 How many memory locations cab be addressed by 8085 microprocessor?
(A) 64 bytes (C) 64 M bytes
(B) 64 K bytes (D) 16 bytes
- 4 Which of the following language gives highest speed of execution.
(A) Machine. (C) Assembly.
(B) High level. (D) None of above.
- 5 Which of the following is two byte instruction?
(A) MVI A, 09H (C) XRA B
(B) JMP C009 H (D) NOP
- 6 Which of the following is a machine control instruction?
(A) RET (C) NOP
(B) JNC (D) ADD



Q-2 Discuss the architecture of 8085 microprocessor in detail.

(07)

OR

Q-2 Write a note on the control signals of the 8085 microprocessor in detail.

(07)

Q-3 List and explain various Data transfer instructions of the 8085 microprocessor with necessary examples.

(07)

OR

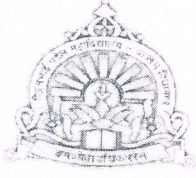
Q-3 Write an assembly language program to load four hexadecimal numbers 12H, 22H, 32H and 42H in registers A, B, C and D respectively. Add the numbers stored in register A and B and store the sum in register H and subtract the register C from D and save the result in register L.

(07)

No of printed pages: 01

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Time: Maximum 10 minutes

Total marks :05

Q-1 Answer the following question with either true or false.

- (i) Address bus is bidirectional bus.
- (ii) Trap interrupt has highest priority.
- (iii) There are eight software interrupts in 8085.
- (iv) MOV A, #00H is three byte instruction.
- (v) HLT instruction is used to stop the processing of instructions.