

V.P. & R.P.T.P. SCIENCE COLLEGE T.Y.B.Sc. (SEMESTER – VI) **INTERNAL TEST: MAR-2018**

		cessors and Assembly La	nguage IARKS: 25
DATE: 15/03/2018	TIME: 11:00am	to 12:50pm W	TARKS: 25
Q.1 Multiple Choice Question			[03]
1. The directive is use	ed to identify the start	of a procedure.	
	c) EXTRN	d) OFFSET	
2. The following instruction r a) NEG BX b) NEG	*	with its 2's complement. d) None of these	
3 compares a byte in DI in ES.	AL or a word in AX	with a byte or word pointed to	by
a) SCASBM b) SCAS	S c)Logical	d) None of these	
Q.2 Answer the following in sh	10rt (Any two)		[04]
 What is Instruction Poi Explain INC instruction What do you know above 	n with example.		
Q.3 Write short note on the 8086 Microprocessor Family overview.			[06]
	OR		
Q.3 Explain in short the overview of 8086 register set.			[06]
Q.4 Explain the following Arithmetic Instructions with examples. (a) ADD/ADC (b) SUB OR			[06]
Q.4 Explain the following Data Transfer Instructions with examples. (a) MOV (b) XCHG			[06]
Q.5 Explain the following conditional and unconditional instructions with exampl (a) JA/JNBE (b) JAE/JNB/JNC			e. [06]
	OR		
Q.5 Explain the following conditional and unconditional instructions with examp (a) JB/JC/JNAE (b) JCXZ			e. [06]