

Extra

V.P. & R.P.T.P SCIENCE COLLEGE
INTERANAL EXAM
T.Y.B.Sc (SEM-VIth) ELECTRONICS
SUB. CODE:-US06CELE03

DATE:-08/03/2017

Time:- 11:00 am to 12:30 pm

MARKS-25

- Q-1 Choose correct answer [3]
1. Rotate accumulator right instruction is _____.
(A) RRC (C) RLC
(B) RAR (D) none of above
 2. _____ is un -conditional jump instruction.
(A) JC (C) JUMP 2009
(B) JNC (D) none of above
 3. _____ is one of the tools for dynamic debugging.
(A) Multi step (C) Memory examine
(B) Single step (D) none of above
- Q-2 Short answer type question.(attempt any two) [4]
1. Define counter and time delay.
 2. What do you mean by debugging?
 3. Define RLC and RRC instructions.
- Q-3 Six byte of data are stored in memory location starting at XX50. Write a programme to add all the data bytes and save the carry generated in a register. Display the entire sum at any two output ports. [6]
- OR
- Q-3 Discuss different rotate and compare instructions. [6]
- Q-4 Write a programme to count continuously in hexadecimal from CF H to 00 H in a system with 0.5 us clock period. Setup a time delay of 1.5 ms between each count and display the number at output port. [6]
- OR
- Q-4 Discuss different STACK and Subroutine instructions with illustration. [6]
- Q-5 Write a programme to convert two digit BCD number stored in memory location to its equivalent binary number. [6]
- OR
- Q-5 Write a programme to convert a binary number stored in memory location to its equivalent un-packed BCD number. [6]

