Expri: 8

V.P. & R.P.T.P. SCIENCE COLLEGE T.Y.B.Sc. (SEMESTER – VI) INTERNAL TEST: MAR-2017

US06CCSC04 : Introduction to Microprocessors and Assembly Languages
DATE: 09/03/2017 TIME: 11:00am to 12:30pm MARKS : 25

Q.1 Multiple Choice Question	[03]
1. The instruction pointer contains a 16-bit offset which tells where in that	
64-Kbyte code segment the byte is to be fetched from.	
(a) next instruction (b) previous instruction (c) middle instruction (d) None	
2. The following instruction adds 1 to contents of BL register.	
(a) INC CX (b) INC BL (c) INC DX (d) None of these	
3. The instruction, Compare string bytes until end of string or until string	
bytes not equal.	
(a) REPE CMPSB (b) REPT CMPSB (c) REPNB CMPSB (d) None of th	ese
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Q.2 Answer the following in short (Any Two)	[04]
1. What is the Execution Unit in 8086?	
2. Explain the ADD instruction.	
3. Write the full form of JA/JNBE and also state its function.	
Q.3 Explain in short the overview of 8086 register set.	[06]
OR	
Q.3 Explain in detail the Flags Registers.	[06]
Q.4 Explain (a) SUB and (b) CMP instructions with examples.	[06]
OR	
Q.4 Explain (a) IDIV and (b) MUL instructions with examples.	[06]
Q.5 Explain SCAS, SCASB and SCASW instructions with example.	[06]
OR	
Q.5 Explain STOS, STOSB and STOSW instructions with example.	[06]