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EXTRA

V.P. & R.P. T. P.Science College. T.Y.B.S.c (5th Semester) Electronics.

USO5CELEO2

1st Internal

Total marks 25

Date	e 30-09-2016 (Friday)			Time 11am	to 12:30 pi	m
	noose correct answer for the fo			i.		(03)
	(1) serial-in-serial-out.(3) parallel-in-serial-out.	(2) parallel-in-para	allel-out.			
(2		, ,	loft to right on	d right to		
(2) In bidirectional-register data c		len to right and	a right to	•	
	(1) left (3) up	(2) right.	1/2	P. Scie		
(3) Carry generate function CG =	(4) none of these	(/4/	Coll.	*.	
(3	(1) A•B	(2) A+B	() S	IBRARY 5		
	(3) A-B	(4) none of these	1151	8		
	(3) A-B	(4) Hone of these		*/		
72	Short question (Attempt any to	wo)		. Nagas		(4)
	Draw the logic diagram of 4		llel-out simple	shift registe	r	(+)
	2) Explain UART drawing diag		ner-out simple	Still register	l.	
	3) Draw the logic symbol of 74		nin			
(b) blaw the logic symbol of 74	LOOS & labell each	piri.			
O3	List different types of data trai	nsmission in shift re	nister and ev	nlain its work	cina	
QU	in detail.		gister and exp	Siairi its work	XIII 9	(6)
	OR OR					(0)
Q3		of 4-hit controlled hi	iffer register a	nd evnlain if	te	
QU	working in detail.	or 4-bit controlled be	mer register at	na explain it		(6)
	Working in dotail.					(0)
04	Give an account of 4-bit Bidir	ectional register an	d explain its w	orking in det	tail	(6)
α ι	OR	ootional regioter an	a explain ite w	onting in dot	.com.	(0)
04	List applications of shift regist	er and explain any	two in detail			(6)
٠,	Liet applications of start region	or and oxplain any	two in dotain.			(0)
Ω5	Explain TWO' Complements A	Addition and Subtra	ction using pa	rallel adders		(6)
~~	OR	.aaaari aria babila	carr domig pu			(3)
Q5	Give an account of comparato	r.				(6)
-						(-)