

VITHALBHAI PATEL & RAJRATNA P.T. PATEL SCIENCE COLLEGE
VALLABH VIDHYA NAGAR

T.Y. B.Sc. SEM: VI

INTERNAL TEST

SUB: INSTRUMENTATION (VOC.)

SUB CODE: US06CINV05

DATE: 14th march 2014

TIME: 3:30 pm to 5:00 pm

TOTAL MARKS: 30

Q.1 Answer the following in short.(Attempt any Three, each two marks) [06]

- (1) State the different technique of dynamic debugging.
- (2) Define STACK and subroutine.
- (3) List the instruction related to stack.
- (4) Briefly ASCII code.
- (5) Define RAR and RLC instruction.
- (6) Define T-state in micro processor.



Q.2 Explain different techniques used for debugging a program. [08]

OR

Q.2 Discuss different arithmetic instruction related to memory with illustration. [08]

Q.3 Discuss different conditional and unconditional CALL and RET instruction of 8085 system with illustration. [08]

OR

Q.3 Write a program to count continuously in hex-decimal from CD H to 00 H in system with 1 micro second clock period. Set up time delay of 1.5 milli second between each count. Display the count at output port. (Take no of T-state = 14) [08]

Q.4 Write a program to convert a BCD number stored in memory location to its equivalent binary number and save the answer in output buffer memory. [08]

OR

Q.4 Write a program to convert a BCD number to its equivalent seven segment LED codes. The codes for the common cathode LED are stored in memory. [08]

- : All the best: -