

VITHALBHAI PATEL & RAJRATNA P.T. PATEL SCIENCE COLLEGE
VALLABH VIDHYA NAGAR

T.Y. B.Sc. SEM: VI
SUB: ELECTRONICS
SUB CODE: US06CELE02

INTERNAL TEST

DATE: 11th March 2014
TIME: 3:30 pm to 5:00 pm
TOTAL MARKS: 30

Q.1 Answer the following in short.(Attempt Three, each two marks) [06]

- (1) Draw the circuit weighed resistor type DAC
- (2) Draw the output of counter type ADC.
- (3) Draw the operational diagram of successive approximation converter.
- (4) List the A to D converter specification and explain any one in brief.
- (5) Draw the diagram of technologies used in the fabrication of ROM(read only memory).
- (6) Draw the ROM block diagram.

Q.2 Draw the neat labeled diagram of IC ADC 0801 and explain importance of each pin in details. [08]

OR

Q.2 Give an account of Voltage to frequency type A/D converter. [08]

Q.3 Draw a neat circuit of R-2R ladder type DAC and explain it working for any one condition of input. [08]

OR

Q.3 Give an account of counter type A/D converter. [08]

Q.4 Give an account of ROM timing in detail with necessary diagram. [08]

OR

Q. 4 Give an account of RAMs, ROMs and PROMs. [08]

-: ALL THE BEST:-

