

V.P & R.P.T.P SCIENCE COLLEGE

Internal test (T.Y.B.Sc. Sem-V)

07/10/2013

Signal Conditioning & Communication

3:30 to 5:00 PM

Monday

US05CINV06

Total Marks: 30

**Q-1 Choose Correct Answer**

[6]

- (1) In a Ring counter data is \_\_\_\_\_.  
(A) Circulated (B) Steady (C) No data (D) none of these
- (2) Registers are made of \_\_\_\_\_.  
(A) Flip Flop (B) Diode (C) Transistor (D) none of these
- (3) Flash type A/D converter is \_\_\_\_\_.  
(A) Fastest (B) Slowest (C) Medium (D) None of these
- (4) In V/T type A/D converter \_\_\_\_\_ is constant.  
(A) Frequency (B) Time (C) Current (D) None of these
- (5) The Schmitt trigger circuit has UTP and \_\_\_\_\_.  
(A) UTP (B) LTP (C) UTP and LTP (D) None of these.
- (6) Modems are classified as \_\_\_\_\_.  
(A) DCE (B) DAC (C) ADC (D) None of these.

**Q-2 Answer the Questions. (Any Three, each two marks)**

[6]

- (1) Draw the operation diagram of successive approximation converter.
- (2) Draw the Neat Diagram of flash type A/D converter.
- (3) Write a truth-table of tri-state switch and explain it.
- (4) Draw the neat diagram of 4-bit weighted resistor type D/A converter and explain in brief.
- (5) Explain FSK drawing figure.
- (6) Explain Asynchronous and Synchronous operation.

Q-3 Give an account of Schmitt -trigger as an interface circuit.

[6]

OR

Q-3 Give an account of Modems and Interfaces.

[6]

Q-4 Explain in detail Voltage to frequency type A/D converter.

[6]

OR

Q-4 Explain in detail working of counter type A/D converter.

[6]

Q-5 Give an account of tri-state switch.

[6]

OR

Q-5 Give an account of R-2R Ladder type D/A converter.

[6]

\*\*\*\*\*All the best\*\*\*\*\*

