V.P & R.P.T.P SCIENCE COLLEGE
V.V.NAGAR
T.Y.B.Sc (5th sem.) Internal test

Date: 05/10/2013   Subject: Instrumentation (V)   Time: 3:30 to 5:00p.m

Subject code: US05CINV05   Total Marks: 30

Q-1   Choose correct answer from given: [6]

(1)   The address buses of 8085 up contain _____ bits.
    (A) 4   (C) 8
    (B) 32   (D) 64

(2)   Following are control signals of 8085 up.
    (A) S_0 and S_1 (C) SOD and SID
    (B) $\overline{WR}$ and $\overline{RD}$ (D) none of these.

(3)   In 8085 up SUB instruction byte size is _____.
    (A) 1byte   (C) 3byte
    (B) 2byte   (D)none of above

(4)   Which of the following is 2 byte instruction?
    (A) MVI C,82H   (C) JMP2098
    (B) MOV B,C   (D) none of these.

(5)   _____ is machine control instruction.
    (A) RET   (C) JNC
    (B) NOP   (D) none of above

(6)   _____ IS 16 bit register in 8085 up.
    (A) Flag register   (C) Stack pointer
    (B) Accumulator   (D) none of these.

Q-2   Short answer type questions. (attempt any three)   [6]

(1)   State different addressing mode of 8085.
(2)   List pins of interrupt control section of 8085.
(3)   State different arithmetic instruction related to memory.
(4)   Define programme and software.
(5)   State characteristics of logical instruction.
(6)   What is the difference between DCR and DCX instruction?

Q-3   Discuss the concept of Generating control signals.   [6]
OR
Q-3   Draw architecture block diagram of 8085 up and discuss each section of it.   [6]

Q-4   Describe classifications of instructions according to word size with illustration.   [6]
OR
Q-4   Discuss method of writing, assembling and executing a program with suitable   [6]
example.

Q-5   Enlist different arithmetic and logical instruction with suitable illustration.   [6]
OR
Q-5   Write a program to load 8BH and 6FH in register C and D respectively. Now   [6]
increment content of C than add both the number and display the sum at out put
port 1.

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