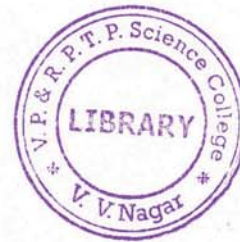


Q.1 Multiple Choice Questions:

03

1. The output of X-OR gate is high
 - (i) When both the inputs are high
 - (ii) When both the inputs are low
 - (iii) When both the inputs are complementary
2. Toggle means
 - (i) SET
 - (ii) Complement of previous state
 - (iii) RESET
3. A counter is a circuit that counts
 - (i) Number of clock pulses that hit the Counter
 - (ii) Time period of Clock Pulse
 - (iii) None of above



Q. 2 Answer any two questions in short.

04

1. Draw LSTTL NAND gate circuit.
2. What are advantages of D Flip Flop over RS Flip Flop?
3. What is the advantage of combinational counter?

Q.3 Describe in detail logic specifications.

06

OR

Q.3 Describe in detail working of TTL NAND gate with neat diagram.

06

Q.4 Describe in detail Edge triggered D Flip Flop.

06

OR

Q.4 Describe fully JK Flip Flop.

06

Q.5 Explain working of Mod 8 serial counter. Draw waveform diagram also.

06

OR

Q.5 Explain working of Mod 5 Asynchronous counter.

06

*****BEST OF LUCK *****