

V.P. & R.P.T.P SCIENCE COLLEGE

INTERNAL EXAM-2017

S.Y.B.Sc. (4TH SEM) ELECTRONICS - US04CELE01

Date -09/03/2017

Time:-3:00 pm to 4:30 pm

- Q-1 Choose correct answer [3]
1. The capacitance between connecting wires and ground is called _____.
(A) stray capacitance (C) inductance
(B) resistance (D) none of above
 2. Self bias FET biasing circuit is _____, than fix bias circuit.
(A) good (C) not better
(B) not good (D) none of above
 3. _____ bias type of biasing is used in common source amplifier circuit.
(A) potential divider (C) self
(B) fixed (D) none of above



- Q-2 Short answer type question.(attempt any two) [4]
1. List different FET parameters.
 2. Draw the self bias circuit for n-channel FET.
 3. Draw the input and output wave form for common source amplifier circuit.

- Q-3 Draw the frequency response curve for transistor amplifier and explain it. And also explain why the gain of an amplifier false off at upper and lower frequency ends. [6]

OR

- Q-3 Give an account of fixed voltage bias circuit for FET. [6]

- Q-4 Draw the potential divider biasing circuit for n-channel FET, and explain its working. [6]

OR

- Q-4 Give an account of n-channel enhancement MOSFET. [6]

- Q-5 Draw a neat circuit diagram for common source amplifier circuit, and explain its working in detail with necessary waveforms. [6]

OR

- Q-5 Draw a neat circuit diagram for common drain amplifier circuit, and explain its working in detail with necessary waveforms. [6]
